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FACSIMILE COVER SHEET

To Examiner: Walter Lee Lindsay Jr.	Total Pages Sent: 4
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Facsimile Number: (571) 273-8300	Transmission Date: October 20, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Chen, *et al.* Docket No.: TSM03-0586
Serial No.: 10/723,771 Art Unit: 2812
Date Filed: November 26, 2003
TITLE: Method Of Fabricating High Voltage Transistor

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- Information Disclosure Statement (1 original and 1 copy)
- Form PTO/SB/08A with 4 references cited (1 page)

Respectfully submitted,



Kristy Engeldahl
Legal Assistant

Confirmation Respectfully Requested

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Commissioner for Patents
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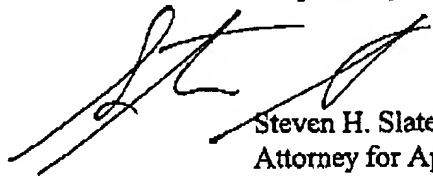
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10/21/2005 HGBREM1 00000084 501065 10723771
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Respectfully submitted,



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PTO/SB/08a (08-03)

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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Application Number	10/723,771
				Filing Date	November 26, 2003
				First Named Inventor	Chen, et al.
				Art Unit	2812
				Examiner Name	Walter Lee Lindsay Jr.
				Attorney Docket Number	TSM03-0586
Sheet	1	of	1		

[illegible][illegible]

Examiner Signature	Date Considered
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